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APPLICANTS

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** CONTINUING DATA ***** *Qun*
 This appln claims benefit of 60/268,827 02/13/2001

** FOREIGN APPLICATIONS ***** *Qun*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 3
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Verified and Acknowledged *Qun*
 Allowance Examiner's Signature Initials

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TITLE
 Matched instruction set processor systems and method, system, and apparatus to efficiently design and implement matched instruction set processor systems by mapping system designs to re-configurable hardware platforms

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